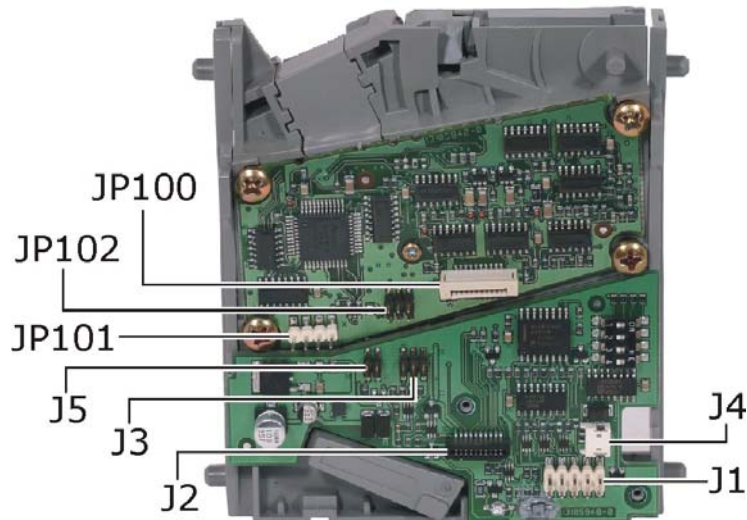


## 6. DIAGRAMS AND PIN OUT



Connector J1 on the Outlet module		
Pins	Function	Notes
Pin 1	+ 12 Vdc	Minimum: + 10 Vdc; Maximum: +15 Vdc
Pin 2	GND	
Pin 3	Data line D6	Open collector, transistor NPN. On X5 validators this Pin is used to classify coins.
Pin 4	Data line D0	Open collector, transistor NPN
Pin 5	Data line D5	Open collector, transistor NPN. On X5 validators this Pin is used to classify coins.
Pin 6	General inhibition	
Pin 7	Data line D2	Open collector, transistor NPN
Pin 8	Data line D1	
Pin 9	Data line D3	
Pin 10	Data line D4	

0 Vdc at 0.7 Vdc - logic level zero

>4 Vdc, maximum voltage or **Pin** open: Logic level one