

## GENERAL DETAILS

### DC Power Supply :

12-24 Vdc regulated  
Range: + 11.5 to + 24 VDC  
Quiescent current: 100 mA @ 12.0 Vdc  
Peak current: 500 mA @ 12.0 Vdc

OR

### AC Power Supply:

12-24 Vac Nominal  
Range: 12-32 Vac

### Coin Output:

The Coin credit pulse(s) can be assigned to any output line. Typically lines 1 to 5  
Pulse width & duty cycle are programmable. Tolerance + 2mSec, - 2mSec  
Logic Open Collector NPN, 200 mA

### Enable/Inhibit Input:

The following SP Inhibit logic states and voltage limits can be set under software.

#### Default State

Inhibit  $2.0v < V_{inh} < V_{in}$  Supply  
Enable  $V_{inh} < 0.8v$  or not connected

### Credit Output :

The Credit, (or Accumulator output) can be assigned to any output line. Typically line 6  
Logic Open Collector, NPN 200mA

### Alarm Output :

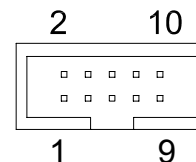
The Alarm output can be assigned to any output line. Typically line 7  
Logic Open Collector NPN, 200mA

### Sorter Output :

The Sorter output can be assigned to any output line. Typically lines 8,9&10 on the Utility Port  
Logic Open Collector NPN, 200mA

## 10 WAY IDC PARALLEL PORT DESCRIPTION

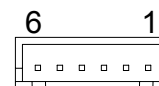
Pin No.	Industry Standard	SP Standard with Credit
1	0Vdc (Neutral-ac)	0Vdc (Neutral-ac)
2	+Vdc (Active-ac)	+Vdc (Active-ac)
3	Coin 5 Output	Coin 5 Output
4	Coin 6 Output	Credit Output (or Coin Output 6)
5	Alarm	Alarm (or Coin Output 7)
6	Inhibit	Inhibit
7	Coin 1 Output	Coin 1 Output
8	Coin 2 Output	Coin 2 Output
9	Coin 3 Output	Coin 3 Output
10	Coin 4 Output	Coin 4 Output



Connector to suit:  
10 Pin 0.1" IDC

## 6 PIN JST UTILITY PORT DESCRIPTION

Pin No.	SP Standard with Sorter
1	Vin Refer Model Description
2	Not Used
3	Sort Output 1 (or Output Line 8)
4	Sort Output 2 (or Output Line 9)
5	Gnd.
6	Sort Output 3 (or Output Line 10)



Connector to suit:  
JST – XH-6